DYNAMIC ENGINEERING

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Est. 1988

Statement of Volatility
PN: PC104p4IP & PCI-1044IP

Manufacturer Part Number: PC104p4IP & PCI-1044IP, includes all options (-ROHS, -CC, etc.)

Manufacturer Part Description: PC104p/PCI-104 Stack adapter for IP Modules.

Memory Type: FPGA and FLASH/PROM

Memory Size: FPGA Xilinx: Internal registers for configuration.

FLASH : 4MBIT. Memory to provide power on configuration to FPGA. Not user accessible.

PROM: current to be replaced by FLASH. Not user accessible.

Volatility: FLASH is non-volatile.   FPGA features are cleared with power cycle [SRAM based].

User Accessible: individual registers etc. are programmable to control operation of the device. No system data stored as likely side effects of reprogramming device.

Clearing Procedure: Power cycle and PCI reset.

Notes or Warnings: None