



DYNAMIC ENGINEERING

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Est. 1988

Statement of Volatility: PCIe-SpaceWire

For the PCIe-SpaceWire design the only device with non-volatile memory is the FLASH used to load the Xilinx device. The FLASH is programmed via JTAG and is not reprogrammable in system unless specific steps are taken to do so – outside of the capabilities of the PCIe-SpaceWire card. In most systems the FLASH is not considered a data security threat as the data does not pass through the FLASH and the FLASH is only used to reload the Xilinx [FPGA].