

8 7 6 5 4 3 2 1

D

D

C

C

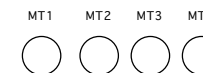
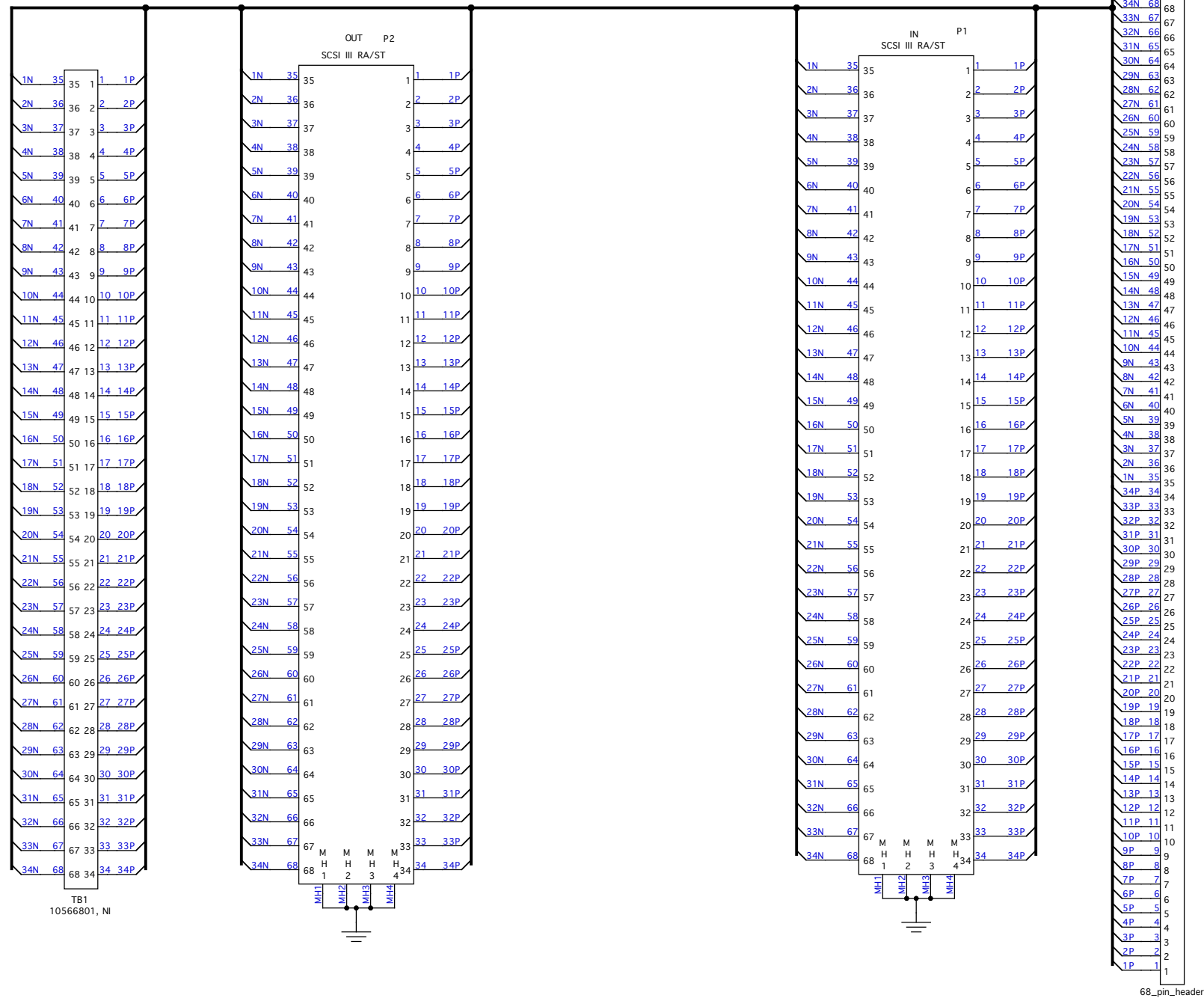
B


B

A

A

RevD, 3-27-08: Updated LED, silk, fabnotes, contact info on PCB.  
 No schematic changes  
 Rev E 10/16/12: update footprint for SCSI, width change in fab, add reference plane tie reference plane to shield via mounting holes of SCSI  
 RevF, 10/28/15: Ported from HDEterm68 reduced to most common build options.





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Design: HDEterm68\_revF1.cct  
 Sheet: \_\_\_\_\_  
 Engineer: Keith Leisses  
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 Sheet 1 of 1

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