

DYNAMIC ENGINEERING

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PCI2PMC-ME-HC Features	
Size	Half size PCI card.
PMC compatible slot	1 PMC Slot provided.
Clocks	PCI bus can operate at 66 or 33 MHz. The PMC must be 66 MHz capable for 66 MHz operation to work properly. M66EN has a user shunt to allow or disable 66 MHz. operation.
Access Width	Standard PCI byte lanes supported for byte, word and long access dependent on installed PMC. 32 bit operation supported.
Software Interface	PMC register definitions as defined by installed hardware. No software set-up required by PCI2PMC.
Interrupts	INTA, B, C, D routed to PCI connector from PMC.
Signal Conditioning	AD signals are series terminated and Schottky clamping Diodes on critical PCI signals. Zero delay buffer for PCI clock distribution.
Power	+5, +3.3, +12, -12V supplied to PMC.
VIO	PCI IO Voltage is set by the PCI backplane. VIO is routed from the PCI connector to the PMC.
Thermal	The PCI2PMC has a cut-out to support increased airflow over the PMC component side.
IO Interface	Front Bezel IO supported at PCI bracket.
LEDs	+3V, +5V, +12V, -12V and Busmode 1.

